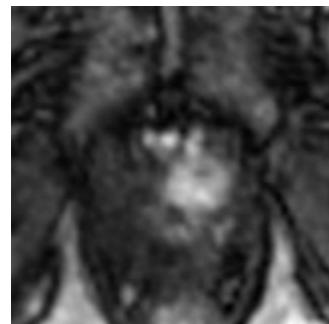
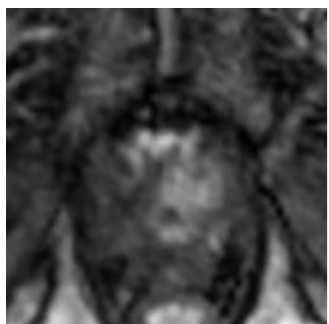


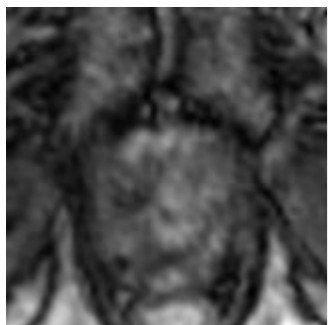
DCE-1



DCE-2



DCE-3



x_0
DCE-Contrast
Phase

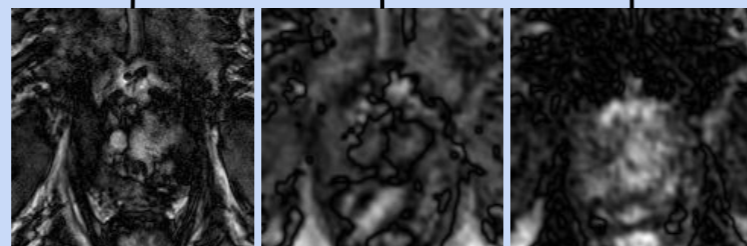
Reverse process

 z_0  z_1 

.....

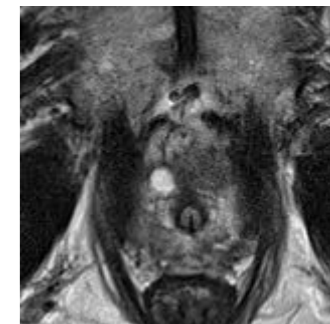
 z_{12} 

...

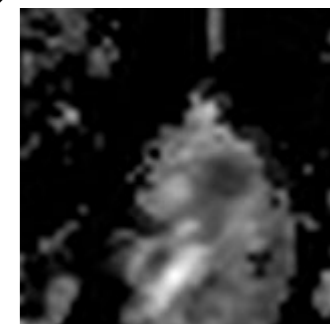
 z_{15} 
 $\otimes \eta_1$
 $\otimes \eta_3$
 $\otimes \eta_{12}$
 $\otimes \eta_{15}$
Residuals - e_0

Forward process

T2-w



ADC



Pre-contrast



y_0
Non-contrast
Multimodal Inputs



Denotes Multiplication